

"Valter Minute" wrote:

"Dean Ramsier" <ramsiernospam@xxxxxxxxxxx> wrote in
[news:e\\$gqyb5\\$HHA.748@xxxxxxxxxxxxxxxxxxxxxxxxxxxxx](mailto:news:e$gqyb5$HHA.748@xxxxxxxxxxxxxxxxxxxxxxxxxxxxx):

And make sure no exceptions occur. That means no page faults, stack faults etc. Note that while you have interrupts off, nothing else (including the OS timer) will run. Keeping in mind that the operation can take several *seconds* to complete (the erase, anyway) the system can be disabled for quite a long time. To do this properly is a far more difficult task than what you probably have in mind.

IIRC some flash memories (Intel's strata flash?) allows you to suspend and then resume delete operations and this will allow you to re-enable interrupts often. Obviously this will slow down the process and will make also the task more complex.

You should consider also that an error during the upgrade operation may leave the device in a "bricked" state unless you provide and alternate OS image that can be booted or other upgrade mechanism.

--

Valter Minute
(the reply address of this message is invalid)
(l'indirizzo di reply di questo messaggio non è valido)