

Re: Synchronization between ISR and IST – I2C Based device

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- *From:* "Srivathsan" <srivathsan.a@xxxxxxxxxx>
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Hi Greats

We solved the problem by masking the UART Interrupt on every read / Write Operation.

The logic is

```
Read / Write Smbus()
{
EnterCriticalSection()
InterruptMask(UART_INTERRUPT, TRUE)
SMBus Read / Write Operation
InterruptMask(UART_INTERRUPT, FALSE)
LeaveCriticalSection()
}
```

Now the Interrupt is being masked and the ISR will not occur when the bus is being accessed. Since the UART is producing only Level Interrupt it will not be cleared and will not process any more interrupts. Thus when the bus access is finished the interrupt is served immediately.

Thanks

Mams and Team

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