

Re: IPSM under Windows CE 4.2 on StrongArm 1100 CPU

Source:

<http://www.tech-archive.net/Archive/WindowsCE/microsoft.public.windowsce.platbuilder/2006-08/msg00145.html>

- *From:* <s>
 - *Date:* Tue, 25 Jul 2006 20:27:27 +0100
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Hello,

Dean do you know anything about the BSquare JVM for Windows CE 4.2?
I have been told the following, but don't know how to get hold of it:

" re: JEM-CE

Contact Bsquare. For some reason, their install works with the Win CE device, While the Esmertec or insignia jeode vms from handango do not. My getting a load of the JEM install was coincidental, and had to do with other factors related to an install problem. They normally only sell their product as bundled in a rom image, so you would have to look into packaging their JVM into your Win CE 4.2 update image.

"

Thanks
Simon

"Dean Ramsier" <ramsiernospam@xxxxxxxxxxx> wrote in message
<news:OdVvykyrGHA.2144@xxxxxxxxxxxxxxxxxxxxxxxxxxxxxxxx>

No. IPSM is a monolithic filesystem, and does not follow the standard MS block driver/FSD architecture. It is not compatible with the Storage Manager architecture.

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Dean Ramsier - eMVP
BSQUARE Corporation

<s> wrote in message <news:IL2dnRapmeBHy17ZnZ2dnUVZ8qKdnZ2d@xxxxxxxxxxx>

Thank you for your advise, the ARMV4I instructions indeed caused the failure on my StrongArm 1100.

To get it working I looked at the PSMFSD.LIB file in the IPSM v3.7 for WinCE4.2 with "notepad" and found ARMV4I in the file paths in the binary file. I noticed that the PocketPC and Smartphone versions only have ARMV4

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in them...

Trying the PocketPC version of PSMFSD.LIB works find with either its own DEF file or the longer DEF file from the Windows CE 4.2 directory in IPSM 3.7.

Thanks for the ARMV4I hint!

Final question

Now it is working under Windows CE 4.2 on my SA1100 CPU and Strata J3 flash chips... BUT should the IPSM appear in the StorageManager in the Control Panel? It doesn't on mine...

Best regards
Simon

"Dean Ramsier" <ramsiernospam@xxxxxxxxxxx> wrote in message
news:uZ7Qdk3qGHA.3380@xxxxxxxxxxxxxxxxxxxxxxxxxxxxx

The bx instruction is "branch and exchange". This is not an XScale specific instruction, it's a standard ARM instruction. However, it requires a version of the ARM core that supports interworking (Thumb and normal modes), which MS calls ARMV4I. The bx instruction is the return instruction for every function call, so it's not surprising you got hit immediately. The SA1100 doesn't support Thumb, thus the undefined instruction exception.

Your only recourse is a version that is built specifically for standard ARM, no interworking allowed.

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Dean Ramsier – eMVP
BSQUARE Corporation

"Paul G. Tobey [eMVP]" <p space tobey no spam AT no instrument no spam DOT com> wrote in message
news:uhenME3qGHA.4932@xxxxxxxxxxxxxxxxxxxxxxxxxxxxx

It's not in PB. You have to get it from Intel. Since, when you get it from Intel, you also get Premier support, you

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should ask them for the official line on which version works with SA1100. Are you saying that the exception occurs before your PLAT_DefineArrayParameters() function is called even once?

Paul T.

<s> wrote in message
news:WKGdnT2HJ4LmDSPZRVny2w@xxxxxxxxxx

Hello,

I have tried and tried to get the IPSM 3.7 and 3.8 to work on my old StrongArm SA1100 and Intel Strata flash based system with Windows CE 4.2. It never starts and the debug fails with an "illegal instruction" at an instruction right at the start of the IPSM initialisation code.

The instruction is "bx lr" and I assume this is an Xscale instruction that then fails on my StrongArm...

Does anyone know what was the last version of the IPSM 3.6 or 2.6? to support strongarm CPUs and that someone got working with Windows CE 4.2.

I have asked Intel in the USA and the UK, neither have been able to answer me.

Interestingly, by confusing me further is that this web page says that IPSm is included in Windows CE 4.2, but the Microsoft documentation on

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the Platform Builder
installation says that it is not
there and I
can't find it.

<http://msdn.microsoft.com/embedded/usewinemb/ce/drivers/supdrivers/default>

Thanks
Simon