

# Help in getting application to access I/O space

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I need to write CE app to manipulate my CPU GPIO outputs. The documentation for the CPU registers that control GPIO signals says that the registers are mapped as offsets into "I/O Space". The base address is 0xF0.

So the question is – how can my application access these registers?

- can I use `WRITE_PORT_ULONG()`?
- must I map this space using some API?